

**Appln No. 10/537,517**  
**Amdt date October 8, 2008**  
**Reply to Office action of June 24, 2008**

**Amendments to the Abstract**

Please replace the abstract with the following new abstract:

In order to further develop a data processing device, in particular a An electronic memory component, comprising provides a plurality of access-secured sub-areas, in particular a plurality of Each access-secured memory sub-area has areas, each having at least one assigned parameter, (a.sub.n, a.sub.n-1, . . . , a.sub.0), in particular for example, an address, [[,]] The memory and a method of encryptsing at least one the assigned parameters (a.sub.n, a.sub.n-1, . . . , a.sub.0) in particular the address, of the at least one access-secured sub-areas, in particular at least one access secured memory area, of at least one data processing device, in particular at least one electronic memory component, in such a way that on the one hand the security of such devices is increased considerably and on the other hand the associated expense and technical complexity are not too great., it is proposed that the parameter (a.sub.n, a.sub.n-1, . . . , a.sub.0) of The encryption allows access to at least one sub-area be capable of encryption only in certain areas, i.e. in dependence dependent on at least one further sub-area(a'.sub.n, a'.sub.n-1, . . . , a'.sub.1, a'.sub.0).